NSN 5820-01-181-3649

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General Description:

Zcontains a phase-lock loop with programable dividers to generate a 10 to 19.999 mhz signal from a 1 mhz reference signal; also generates 10 hz through 1 khz step sizes in the synthesizer; 2 sma connectors and 1 printed card connector

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Shelf	I Ito:
Juen	LIIE.

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli