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End Application:

RADA-RS-ETA-N/TP

General Description:

Consists of mfi interface, 1; beacon reply processor, 1; beacon control, 1; command interfacce, 1; fast parallel interface, 1; synthetic video beacon defruiter, 1; beacon video quantizer, 1; timing generator, 1; fifo buffer, 1; asp data memory, 1; radar interface, 1; control unit, 1; processing element, 1; program memory, 1; 8086 microprocessor, 1; asp rom, 1; front cover, 1; connector panel, 1; wired backplane assembly, 1; enclosure, 1; dte control panel assembly, 1; cover, bottom, 1; and associated hardware

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

A23900