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Inclosure Material:

Metal

Overall Length:

0.210 inches

Terminal Length:

0.500 inches

Overall Width:

0.230 inches

Joint Electronic Device Engineering Council/jedec/case Outline Designation:

To-71

Component Name And Quantity:

2 transistor

Mounting Method:

Terminal

Terminal Circle Diameter:

0.100 inches

Features Provided:

Hermetically sealed case

Semiconductor Material:

Silicon all transistor

Voltage Rating In Volts Per Characteristic:

50.0 drain to gate voltage all transistor and 50.0 drain to source voltage all transistor and 50.0 reverse gate to source voltage all transistor

Current Rating Per Characteristic:

50.00 milliamperes gate current all transistor

Power Rating Per Characteristic:

500.0 milliwatts total power dissipation all transistor

Test Data Document:

80131-release 5157 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

6 uninsulated wire lead

Specification Data:

80131-release 5157 professional/industrial association specification

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fiig:

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