Semiconductor Device Set - Page 1 of 1



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| Inclosu | re Material: |
|-----------|--|
| Plastic a | all transistor |
| Overall | Length: |
| 0.170 in | ches all transistor and 0.210 inches all transistor |
| Termina | al Length: |
| 0.500 in | ches all transistor |
| Internal | Configuration: |
| Field eff | ect all transistor |
| Channe | el Polarity And Control Type (non-core): |
| N-chanr | nel junction type all transistor |
| Joint El | lectronic Device Engineering Council/jedec/case Outline Designation: |
| To-92 al | Il transistor |
| Compo | nent Function Relationship: |
| Matcheo | t de la construcción de la const |
| Compo | nent Name And Quantity: |
| 2 transis | stor |
| Mountir | ng Method: |
| Termina | al all transistor |
| Semico | nductor Material: |
| Silicon a | all transistor |
| Voltage | Rating In Volts Per Characteristic: |
| 25.0 bre | eakdown voltage, gate-to-source, with all other terminals short-circuited to source all transisto |
| Current | Rating Per Characteristic: |
| 9.00 mil | liamperes zero-gate-voltage source current megawatts all transistor |
| Termina | al Type And Quantity: |
| 3 uninsu | ulated wire lead all transistor |
| Shelf Li | ife: |
| N/a | |
| Unit Of | Measure: |
| | |
| Demilita | arization: |
| No | |
| | |
| Fiig: | |