NSN 5962-00-001-7447

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Body Length:
Between 0.337 inches and 0.350 inches
Body Width:
Between 0.200 inches and 0.300 inches
Body Height:
Between 0.008 inches and 0.100 inches
Maximum Power Dissipation Rating:
112.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and medium power and medium speed
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nand
Case Outline Source And Designator:
-0-004-aa joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level
output
Special Features:
Formed leads
Test Data Document:
82577-932044 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:

N/a

Unit Of Measure:

14 flat leads

Shelf Life:

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Demilitarization:

Yes - demil/mli

Fiig:

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