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**Body Length:**

Between 0.240 inches and 0.275 inches

**Body Width:**

Between 0.160 inches and 0.185 inches

**Body Height:**

Between 0.030 inches and 0.070 inches

**Maximum Power Dissipation Rating:**

40.0 milliwatts

**Operating Temperature Range:**

-55.0/+125.0 degrees celsius

**Storage Temperature Range:**

-65.0/+200.0 degrees celsius

**Features Provided:**

Hermetically sealed and monolithic and positive outputs and expandable and high speed

**Inclosure Material:**

Ceramic and glass

**Inclosure Configuration:**

Flat pack

**Output Logic Form:**

Transistor-transistor logic

**Input Circuit Pattern:**

4 wide 3-2-2-3 input

**Design Function And Quantity:**

1 gate, and-or invert

**Case Outline Source And Designator:**

T0-85 joint electron device engineering council

**Terminal Surface Treatment:**

Solder

**Voltage Rating And Type Per Characteristic:**

-1.5 volts power source and 5.5 volts power source

**Time Rating Per Characteristic:**

4.00 nanoseconds propagation delay time, low to high level output and 4.00 nanoseconds propagation delay time, high to low level output

**Terminal Type And Quantity:**

14 flat leads

**Shelf Life:**

N/a

**Unit Of Measure:**

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**Demilitarization:**

Yes - demil/mli

**Fig:**

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