NSN 5962-00-008-0575

Digital Microcircuit - Page 1 of 1



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Body Length:

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Maximum Power Dissipation Rating:

40.0 milliwatts

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and w/totem pole output and high speed

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 gate, nand

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Time Rating Per Chacteristic:

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

07421-349-113-064 standard (includes industry or association standards, individual manufactureer standards, etc.).

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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