# NSN 5962-00-008-5679

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## **Body Length:**

Between 0.240 inches and 0.275 inches

## **Body Width:**

Between 0.160 inches and 0.185 inches

# **Body Height:**

Between 0.030 inches and 0.070 inches

## **Maximum Power Dissipation Rating:**

500.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+125.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and w/enable and negative edge triggered and resettable and positive outputs

#### **Inclosure Material:**

# Ceramic and glass

Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

8 input

#### **Design Function And Quantity:**

2 flip-flop, j-k, clocked

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

7.0 volts power source

#### **Time Rating Per Chacteristic:**

20.00 nanoseconds propagation delay time, low to high level output and 20.00 nanoseconds propagation delay time, high to low level output

#### **Test Data Document:**

13973-862253 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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