NSN 5962-00-008-6866

Digital Microcircuit - Page 1 of 1

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| Body Length: |
|--|
| Between 0.330 inches and 0.350 inches |
| Body Width: |
| Between 0.140 inches and 0.275 inches |
| Body Height: |
| Between 0.050 inches and 0.080 inches |
| Maximum Power Dissipation Rating: |
| 32.5 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and expandable and positive outputs |
| Inclosure Material: |
| Glass and metal |
| Inclosure Configuration: |
| Flat pack |
| Output Logic Form: |
| Diode-transistor logic |
| Input Circuit Pattern: |
| Dual 4 input |
| Design Function And Quantity: |
| 2 gate, nand-nor |
| Time Rating Per Chacteristic: |
| 80.00 nanoseconds propagation delay time, low to high level output |
| Precious Material And Location: |
| Terminals gold and body gold |
| Test Data Document: |
| 80063-sm-b-586420 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, |
| etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |