NSN 5962-00-009-1347

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Body Length:
Between 0.750 inches and 0.795 inches
Body Width:
Between 0.245 inches and 0.300 inches
Body Height:
Between 0.120 inches and 0.195 inches
Maximum Power Dissipation Rating:
100.0 milliwatts
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and w/enable and edge triggered and w/clear and presettable
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 5 input
Design Function And Quantity:
2 flip-flop, j-k, clocked
Case Outline Source And Designator:
-0-001-ag joint electron device engineering council
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
10236-7100041 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig: