## NSN 5962-00-011-3010

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| Body Length:   |
|--|
| Between 0.660 inches and 0.785 inches  |
| Body Width:  |
| Between 0.220 inches and 0.280 inches  |
| Body Height:   |
| Between 0.140 inches and 0.180 inches  |
| Operating Tempurature Range:   |
| +0.0/+75.0 degrees celsius   |
| Storage Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Features Provided:   |
| Monolithic and positive outputs  |
| Inclosure Material:  |
| Plastic  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Diode-transistor logic   |
| Input Circuit Pattern:   |
| 7 input  |
| Design Function And Quantity:  |
| 1 flip-flop, j-k/r-s, clocked  |
| Case Outline Source And Designator:  |
| T0-116 joint electron device engineering council   |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| 8.0 volts power source   |
| Time Rating Per Chacteristic:  |
| 75.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level |
| output   |
| Terminal Type And Quantity:  |
| 14 printed circuit   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
| <del></del>  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |