# NSN 5962-00-028-0981

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# **Body Width:**

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

60.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and high speed and w/enable and negative edge triggered

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

10 input

#### Design Function And Quantity:

1 flip-flop, clocked and 1 flip-flop, j-k, and/or input

# Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 5.5 volts power source

#### Time Rating Per Chacteristic:

11.00 nanoseconds propagation delay time, low to high level output and 9.00 nanoseconds propagation delay time, high to low level output

#### **Terminal Type And Quantity:**

14 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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