NSN 5962-00-050-9670

Digital Microcircuit - Page 1 of 1



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Body Length:

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Maximum Power Dissipation Rating:

45.0 milliwatts

Operating Tempurature Range:

+0.0/+75.0 degrees celsius

Storage Tempurature Range:

-65.0/+200.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and positive outputs and medium speed

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Triple 3 input

Design Function And Quantity:

3 gate, nand

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Time Rating Per Chacteristic:

10.00 nanoseconds propagation delay time, low to high level output

Test Data Document:

97384-5339-090546 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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