## NSN 5962-00-066-9583

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| Body Length:  |
|---|
| Between 0.695 inches and 0.705 inches   |
| Body Width:   |
| Between 0.235 inches and 0.265 inches   |
| Body Height:  |
| 0.100 inches  |
| Operating Tempurature Range:  |
| -55.0/+125.0 degrees celsius  |
| Storage Tempurature Range:  |
| -65.0/+200.0 degrees celsius  |
| Features Provided:  |
| Hermetically sealed and monolithic and positive outputs   |
| Inclosure Material:   |
| Ceramic   |
| Inclosure Configuration:  |
| Dual-in-line  |
| Output Logic Form:  |
| Diode-transistor logic  |
| Input Circuit Pattern:  |
| Quad 2 input  |
| Design Function And Quantity:   |
| 4 gate, nand-nor  |
| Terminal Surface Treatment:   |
| Solder  |
| Voltage Rating And Type Per Characteristic:   |
| -1.5 volts power source and 5.5 volts power source  |
| Test Data Document:   |
| 89661-128c682 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc. |
| excludes any specification, standard or other document that may be referenced in a basic governing drawing)                             |
| Terminal Type And Quantity:   |
| 14 printed circuit  |
| Shelf Life:   |
| N/a   |
| Unit Of Measure:  |
| <del>-</del>  |
| Demilitarization:   |
| Yes - demil/mli   |
| Fiig:   |
| A458a0  |
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