NSN 5962-00-099-9795

Digital Microcircuit - Page 1 of 1



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The state of the s
Body Length:
Between 0.337 inches and 0.350 inches
Body Width:
Between 0.337 inches and 0.350 inches
Body Height:
Between 0.050 inches and 0.080 inches
Maximum Power Dissipation Rating:
52.0 milliwatts
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and positive outputs and high speed and w/totem pole output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 gate, nand buffer
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
12.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low lev
output
Test Data Document:
05869-717129 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:
Vas - damil/mli

Fiig: A458a0