NSN 5962-00-104-1665

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Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Operating Tempurature Range:
+0.0/+75.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Expandable and monolithic and positive outputs
Inclosure Material:
Plastic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Diode-transistor logic and transistor-transistor logic
Input Circuit Pattern:
Dual 3 input
Design Function And Quantity:
2 expander and 2 gate, or
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Time Rating Per Chacteristic:
70.00 nanoseconds propagation delay time, low to high level output and 80.00 nanoseconds propagation delay time, high to low level
output
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiia: