# NSN 5962-00-109-5879

Digital Microcircuit - Page 1 of 1



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# **Body Length:**

Between 0.240 inches and 0.275 inches

# Body Width:

Between 0.160 inches and 0.185 inches

# **Body Height:**

Between 0.030 inches and 0.070 inches

# **Maximum Power Dissipation Rating:**

40.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and w/enable and negative edge triggered and presettable and resettable and

w/storage

# Inclosure Material:

Ceramic and glass

#### Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

10 input

#### **Design Function And Quantity:**

1 flip-flop, clocked and 1 flip-flop, j-k, and input

#### Case Outline Source And Designator:

T0-85 joint electron device engineering council

#### **Time Rating Per Chacteristic:**

25.00 nanoseconds propagation delay time, low to high level output and 13.00 nanoseconds propagation delay time, high to low level output

#### Shelf Life:

N/a

# Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

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