NSN 5962-00-110-6425

Digital Microcircuit - Page 1 of 2



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Body Length:
Between 0.337 inches and 0.350 inches
Body Width:
Between 0.200 inches and 0.300 inches
Body Height:
Between 0.008 inches and 0.100 inches
Maximum Power Dissipation Rating:
24.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and positive outputs and medium speed
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Diode-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nand
Case Outline Source And Designator:
-0-004-aa joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output
Test Data Document:
22915-st32904 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:

Demilitarization: Yes - demil/mli

NSN 5962-00-110-6425Digital Microcircuit - Page 2 of 2



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