# NSN 5962-00-111-1285

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## **Body Length:**

Between 0.710 inches and 0.770 inches

## Body Width:

Between 0.240 inches and 0.260 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# Maximum Power Dissipation Rating:

52.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### Features Provided:

Monolithic and positive outputs and w/totem pole output

#### Inclosure Material:

Plastic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Dual 4 input

# **Design Function And Quantity:**

2 gate, nand buffer

#### Case Outline Source And Designator:

-0-001-aa joint electron device engineering council

#### **Time Rating Per Chacteristic:**

22.00 nanoseconds propagation delay time, low to high level output

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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