NSN 5962-00-117-4572

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Body Length:
Between 0.330 inches and 0.350 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
Between 0.030 inches and 0.070 inches
Maximum Power Dissipation Rating:
24.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Positive outputs and hermetically sealed and monolithic
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Diode-transistor logic
Input Circuit Pattern:
Dual 5 input
Design Function And Quantity:
2 gate, nand-nor
Case Outline Source And Designator:
50-88 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Test Data Document:
80249-330303 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

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