# NSN 5962-00-118-2577

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.660 inches and 0.785 inches

## Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

## **Maximum Power Dissipation Rating:**

48.8 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### End Application:

An/spn-10 and an/spn-42

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and low power and medium speed

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

## Output Logic Form:

Diode-transistor logic

### Input Circuit Pattern:

Triple 3 input

### **Design Function And Quantity:**

3 gate, nand-nor

### Case Outline Source And Designator:

T0-116 joint electron device engineering council

### Time Rating Per Chacteristic:

80.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

### **Test Data Document:**

80070-17-12064 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Shelf Life:

N/a

## Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

# Fiig:

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