## NSN 5962-00-121-8822

Digital Microcircuit - Page 1 of 1



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view Offiline at https://defobasegroup.com/hist//0502-00-121-0022
Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Maximum Power Dissipation Rating:
200.0 milliwatts
Operating Tempurature Range:
-30.0/+75.0 degrees celsius
Storage Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and w/enable and resettable and positive outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 flip-flop, j-k, clocked
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.0 volts power source and 18.0 volts power source
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:

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