# NSN 5962-00-136-0258

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# **Body Length:**

0.260 inches

## **Body Width:**

0.260 inches

# **Body Height:**

Between 0.030 inches and 0.060 inches

#### **Operating Tempurature Range:**

-55.0 to 125.0 degrees celsius

#### Storage Tempurature Range:

-65.0 to 150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and w/clock

#### **Inclosure Material:**

Ceramic

#### Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Dual 4 input

## **Design Function And Quantity:**

2 flip-flop, j-k

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

8.0 volts power source

## **Time Rating Per Chacteristic:**

100.00 nanoseconds propagation delay time, low to high level output and 55.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

05869-717320-21 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Terminal Type And Quantity:

14 flat leads

#### Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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