NSN 5962-00-136-3221

Digital Microcircuit - Page 1 of 1



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| Body Outside Diameter: |
|---|
| Between 0.335 inches and 0.370 inches |
| Body Height: |
| Between 0.165 inches and 0.185 inches |
| Maximum Power Dissipation Rating: |
| 46.5 milliwatts |
| Operating Tempurature Range: |
| +0.0/+75.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and positive outputs and edge triggered and low power and medium speed and resettable and w/clea |
| Inclosure Material: |
| Glass and metal |
| Inclosure Configuration: |
| Can |
| Output Logic Form: |
| Diode-transistor logic |
| Input Circuit Pattern: |
| 6 input |
| Design Function And Quantity: |
| 1 flip-flop, j-k/r-s, clocked |
| Case Outline Source And Designator: |
| T0-100 joint electron device engineering council |
| Time Rating Per Chacteristic: |
| 35.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level |
| output |
| Precious Material And Location: |
| Terminals gold |
| Precious Material: |
| Gold |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| - |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |