# NSN 5962-00-137-5036

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#### **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

#### **Body Height:**

Between 0.140 inches and 0.180 inches

### **Maximum Power Dissipation Rating:**

36.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and high speed and expandable

#### **Inclosure Material:**

# Ceramic and glass

Inclosure Configuration:

#### Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

4 wide 2-2-2-3 input

#### **Design Function And Quantity:**

1 gate, and-or invert

#### Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### **Time Rating Per Chacteristic:**

7.00 nanoseconds propagation delay time, low to high level output

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

No

#### Fiig:

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