NSN 5962-00-146-5060

Digital Microcircuit - Page 1 of 1

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| Body Length: |
|--|
| Between 0.750 inches and 0.785 inches |
| Body Width: |
| Between 0.245 inches and 0.271 inches |
| Body Height: |
| Between 0.140 inches and 0.180 inches |
| Maximum Power Dissipation Rating: |
| 150.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Monolithic and hermetically sealed and medium speed and positive outputs and w/enable |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| Dual 3 input |
| Design Function And Quantity: |
| 2 decoder, binary to one of four |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 5.5 volts power source |
| Time Rating Per Chacteristic: |
| 30.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level |
| output |
| Terminal Type And Quantity: |
| 16 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| |