# NSN 5962-00-146-5087

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## **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

160.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Monolithic and positive outputs and medium power and medium speed and w/open collector

#### Inclosure Material:

Plastic

## Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Quad 2 input

## **Design Function And Quantity:**

4 gate, nand

## Case Outline Source And Designator:

T0-116 joint electron device engineering council

## Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

## **Precious Material And Location:**

Terminals silver

#### **Precious Material:**

Silver

## Test Data Document:

20551-a308 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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