# NSN 5962-00-147-0798

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# Maximum Power Dissipation Rating:

500.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and medium power and medium speed and asynchronous and synchronous and

edge triggered and w/enable

Inclosure Material:

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

**Diode-transistor logic** 

#### Input Circuit Pattern:

Dual 4 input

#### **Design Function And Quantity:**

2 flip-flop, j-k, clocked and 2 flip-flop, j-k, master slave

# Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

5.5 volts power source

# Time Rating Per Chacteristic:

65.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level

output

#### Test Data Document:

23667-2412939 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Terminal Type And Quantity:

14 printed circuit

# Shelf Life:

N/a

#### Unit Of Measure:

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- Demilitarization:

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