NSN 5962-00-147-4484

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Body Length:
Between 0.750 inches and 0.795 inches
Body Width:
Between 0.245 inches and 0.300 inches
Body Height:
Between 0.120 inches and 0.195 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and resettable and edge triggered and w/enable
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Diode-transistor logic
Input Circuit Pattern:
10 input
Design Function And Quantity:
1 flip-flop, j-k/r-s, clocked, master slave
Case Outline Source And Designator:
-0-001-ag joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
15.0 volts power source
Test Data Document:
96214-537048 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:
Yes - demil/mli

Fiig:

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