# NSN 5962-00-147-7941

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## **Body Length:**

Between 0.660 inches and 0.785 inches

# **Body Width:**

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

255.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs and medium power and medium speed and expandable

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Dual 2 wide 2 input

## **Design Function And Quantity:**

2 gate, and-or invert

## Case Outline Source And Designator:

T0-116 joint electron device engineering council

## **Time Rating Per Chacteristic:**

30.00 nanoseconds propagation delay time, low to high level output and 24.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

08783-37650021 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Shelf Life:

N/a

#### Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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