# NSN 5962-00-163-4125

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## **Body Length:**

Between 0.330 inches and 0.350 inches

## **Body Width:**

Between 0.200 inches and 0.300 inches

## **Body Height:**

Between 0.008 inches and 0.100 inches

## **Maximum Power Dissipation Rating:**

2.6 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### Storage Tempurature Range:

-65.0/+200.0 degrees celsius

### Features Provided:

Low power and monolithic and positive outputs

#### **Inclosure Material:**

## Ceramic and glass

Inclosure Configuration:

Flat pack

## **Output Logic Form:**

Emitter-coupled logic

### Input Circuit Pattern:

Dual 5 input

### **Design Function And Quantity:**

2 gate, nand

### **Case Outline Source And Designator:**

-0-004-aa joint electron device engineering council

### **Terminal Surface Treatment:**

Solder

### Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

### Time Rating Per Chacteristic:

50.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level

output

# **Terminal Type And Quantity:**

14 flat leads

### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

### Fiig:

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