# NSN 5962-00-168-3633

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# **Body Length:**

Between 0.755 inches and 0.785 inches

# **Body Width:**

0.280 inches

# Body Height:

0.180 inches

# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

# **Features Provided:**

Hermetically sealed

#### **Inclosure Material:**

Ceramic and glass

## Inclosure Configuration:

Dual-in-line

# Output Logic Form:

# Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

# **Design Function And Quantity:**

2 flip-flop, j-k, master slave

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.0 volts power source

# **Time Rating Per Chacteristic:**

50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

73168-06-115533-004 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

## Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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