# NSN 5962-00-170-9722

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## **Body Length:**

Between 0.240 inches and 0.260 inches

## **Body Width:**

Between 0.240 inches and 0.260 inches

# **Body Height:**

Between 0.030 inches and 0.070 inches

## **Maximum Power Dissipation Rating:**

40.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs and negative outputs

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Dual 4 input

## **Design Function And Quantity:**

2 gate, nand

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## Time Rating Per Chacteristic:

80.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

## **Precious Material And Location:**

Terminal surface gold

#### Precious Material:

Gold

## Test Data Document:

24384-36001204 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Terminal Type And Quantity:

14 flat leads

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

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