# NSN 5962-00-173-3499

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#### **Body Length:**

Between 0.240 inches and 0.275 inches

#### Body Width:

Between 0.160 inches and 0.185 inches

#### **Body Height:**

Between 0.030 inches and 0.070 inches

# **Maximum Power Dissipation Rating:**

110.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+200.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs and high speed and w/enable and negative edge triggered and presettable and resettable

# Inclosure Material:

Ceramic and glass

# Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

10 input

#### **Design Function And Quantity:**

1 flip-flop, clocked and 1 flip-flop, j-k, and input

#### Case Outline Source And Designator:

T0-85 joint electron device engineering council

## **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

## Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

## Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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