NSN 5962-00-175-5958

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Body Length:
Between 0.330 inches and 0.350 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
Between 0.030 inches and 0.070 inches
Maximum Power Dissipation Rating:
104.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and positive outputs and high speed
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nand
Case Outline Source And Designator:
T0-88 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 6.0 volts power source
Time Rating Per Chacteristic:
13.00 nanoseconds propagation delay time, low to high level output and 13.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:

Yes - demil/mli

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