# NSN 5962-00-175-8954

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## **Body Length:**

Between 0.330 inches and 0.350 inches

# **Body Width:**

Between 0.240 inches and 0.260 inches

# **Body Height:**

Between 0.030 inches and 0.070 inches

# **Maximum Power Dissipation Rating:**

45.2 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and positive outputs

#### **Inclosure Material:**

# Ceramic and glass

Inclosure Configuration:

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

Dual 5 input

## **Design Function And Quantity:**

2 gate, nand

## Case Outline Source And Designator:

T0-88 joint electron device engineering council

## **Time Rating Per Chacteristic:**

50.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output

## **Precious Material And Location:**

Terminal surface gold

## Precious Material:

Gold

## Test Data Document:

09128-d34010894 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

# Fiig:

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