## NSN 5962-00-176-2724

Memory Microcircuit - Page 1 of 2



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Body Length:
Between 0.340 inches and 0.440 inches
Body Width:
Between 0.265 inches and 0.325 inches
Body Height:
Between 0.030 inches and 0.075 inches
Maximum Power Dissipation Rating:
625.0 milliwatts
Operating Tempurature Range:
+0.0/+75.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
58404091248
Features Provided:
Hermetically sealed and monolithic and negative outputs and positive outputs and w/enable and programmable and programmed and bipolar
and w/buffered output and w/decoded output and expandable and wire-or outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
6 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
50.00 nanoseconds propagation delay time, low to high level output
Memory Device Type:
Rom
Test Data Document:
05869-717383 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)

16 flat leads

**Terminal Type And Quantity:** 

Shelf Life:

N/a

**Unit Of Measure:** 

Demilitarization:

Yes - demil/mli

## NSN 5962-00-176-2724

Memory Microcircuit - Page 2 of 2



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