# NSN 5962-00-184-8224

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## **Body Length:**

Between 0.710 inches and 0.870 inches

### Body Width:

Between 0.240 inches and 0.260 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

140.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and positive outputs and w/totem pole output

#### Inclosure Material:

Plastic

### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

4 input

### Design Function And Quantity:

1 decoder, binary coded decimal to one of ten and 1 decoder, four to ten line

### Case Outline Source And Designator:

-0-001-ag joint electron device engineering council

### **Time Rating Per Chacteristic:**

30.00 nanoseconds propagation delay time, low to high level output

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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