# NSN 5962-00-190-3230

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

100.0 milliwatts

## **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

# Storage Tempurature Range:

-55.0/+125.0 degrees celsius

## Features Provided:

Monolithic and positive outputs and high speed and darlington-connected and w/enable and resettable and negative edge triggered

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

8 input

## **Design Function And Quantity:**

2 flip-flop, j-k, clocked

## Case Outline Source And Designator:

T0-116 joint electron device engineering council

## **Test Data Document:**

49956-531593 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

No

## Fiig:

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