# NSN 5962-00-190-4023

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

30.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/totem pole output

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Triple 3 input

#### **Design Function And Quantity:**

3 gate, nand

# Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### Time Rating Per Chacteristic:

22.00 nanoseconds propagation delay time, low to high level output

#### Test Data Document:

81349-mil-std-883 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

## Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

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