## NSN 5962-00-191-1980

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Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and positive outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Diode-transistor logic
Input Circuit Pattern:
Hex 1 input
Design Function And Quantity:
6 inverter, digital
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.0 volts power source
Time Rating Per Chacteristic:
80.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
13973-862703 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification formation
excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental
and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
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**Demilitarization:** Yes - demil/mli