# NSN 5962-00-191-1995

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# **Body Width:**

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and w/enable and resettable and complementary outputs and low power

## Inclosure Material:

Ceramic

## Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

9 input

## **Design Function And Quantity:**

1 shift register, parallel in, parallel out

# **Terminal Surface Treatment:**

Gold

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## **Precious Material And Location:**

Terminal surface gold

## **Precious Material:**

Gold

## **Test Data Document:**

13973-862523 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# **Terminal Type And Quantity:**

16 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

#### Fiig:

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