# NSN 5962-00-200-9675

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

195.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

# Storage Tempurature Range:

-65.0/+150.0 degrees celsius

# **Features Provided:**

Hermetically sealed and monolithic and positive outputs and high speed and medium power

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

Hex 1 input

#### **Design Function And Quantity:**

6 inverter, digital

# **Case Outline Source And Designator:**

T0-116 joint electron device engineering council

# **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

# Time Rating Per Chacteristic:

17.00 nanoseconds propagation delay time, low to high level output and 17.00 nanoseconds propagation delay time, high to low level

output

#### Test Data Document:

42470-007-1695301 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

14 printed circuit

#### Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

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