NSN 5962-00-203-5503

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Body Length:

Between 0.240 inches and 0.275 inches

Body Width:

Between 0.165 inches and 0.185 inches

Body Height:

Between 0.030 inches and 0.070 inches

Maximum Power Dissipation Rating:

176.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and high speed and positive outputs

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

Design Function And Quantity:

2 gate, nand

Case Outline Source And Designator:

T0-85 joint electron device engineering council

Test Data Document:

13499-351-7267 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

No

Fiig:

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