NSN 5962-00-208-4243

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\mathbf{Bod}	y Ler	ngth:

Between 0.250 inches and 0.260 inches

Body Width:

Between 0.140 inches and 0.150 inches

Body Height:

Between 0.035 inches and 0.050 inches

Maximum Power Dissipation Rating:

11.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and low power and presettable and resettable and w/clear

Inclosure Material:

Glass and metal

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

9 input

Design Function And Quantity:

1 flip-flop, r-s, clocked and 1 flip-flop, r-s, master slave

Case Outline Source And Designator:

T0-84 joint electron device engineering council

Terminal Surface Treatment:

Gold

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

125.00 nanoseconds propagation delay time, low to high level output and 200.00 nanoseconds propagation delay time, high to low level output

Special Features:

Insulator furnished; mech-pakette furnished

Precious Material And Location:

Terminals gold and body gold

Precious Material:

Gold

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

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			ure:	

Demilitarization:

Yes - demil/mli

Fiig:

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