# NSN 5962-00-224-5286

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# **Body Length:**

Between 0.755 inches and 0.785 inches

# Body Width:

Between 0.245 inches and 0.280 inches

# **Body Height:**

0.180 inches

**Maximum Power Dissipation Rating:** 

10.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/clear and w/preset

#### Inclosure Material:

Ceramic

# Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

9 input

## Design Function And Quantity:

1 flip-flop, j-k, and input master slave

# Case Outline Source And Designator:

To-116 joint electron device engineering council

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

7.0 volts power source

## **Time Rating Per Chacteristic:**

25.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level

# output

# **Terminal Type And Quantity:**

14 printed circuit

## **Specification Data:**

07690-429579-01 manufacturers source control

# Shelf Life:

N/a

## Unit Of Measure:

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- Demilitarization:
- Yes demil/mli

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