NSN 5962-00-229-7192

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Overall Height:
0.325 inches
Body Length:
Between 0.710 inches and 0.770 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
0.180 inches
Maximum Power Dissipation Rating:
10.0 milliwatts
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and positive outputs and w/totem pole output
Inclosure Material:
Plastic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 2 wide 2 input
Design Function And Quantity:
2 gate, and-or invert
Case Outline Source And Designator:
To-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts total supply
Time Rating Per Chacteristic:
11.00 nanoseconds propagation delay time, low to high level output and 11.00 nanoseconds propagation delay time, high to low level
output
Precious Material And Location:
Terminal surfaces silver
Precious Material:
Silver
Terminal Type And Quantity:
14 printed circuit
Shelf Life:

N/a

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Demilitarization:

Yes - demil/mli

Fiig:

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