# NSN 5962-00-236-8835

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

0.180 inches

## **Operating Tempurature Range:**

+0.0 to 70.0 degrees celsius

#### Storage Tempurature Range:

-65.0 to 150.0 degrees celsius

## **Features Provided:**

Monolithic

#### **Inclosure Material:**

Plastic

#### Inclosure Configuration:

Dual-in-line

# **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

8 input

## **Design Function And Quantity:**

1 gate, nand

## Case Outline Source And Designator:

To-116 joint electron device engineering council

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.0 volts power source

## **Time Rating Per Chacteristic:**

29.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

## **Precious Material And Location:**

Terminal surfaces option gold or terminal surfaces option silver

#### **Precious Material:**

Gold or silver

#### **Test Data Document:**

19790-800021 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# **Terminal Type And Quantity:**

14 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

A458a0

