NSN 5962-00-250-3663

Digital Microcircuit - Page 1 of 2



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Bodv	l er	nath:
Douv	LCI	ıuıı.

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Maximum Power Dissipation Rating:

198.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and w/totem pole output and high speed and schottky

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Criticality Code Justification:

Feat

Design Function And Quantity:

4 gate, nand

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

4.50 nanoseconds propagation delay time, low to high level output and 5.00 nanoseconds propagation delay time, high to low level output

Special Features:

Static electricity sensitive

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

NSN 5962-00-250-3663

Digital Microcircuit - Page 2 of 2



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Demilitarization:

Yes - demil/mli

Fiig:

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