NSN 5962-00-252-4530

Digital Microcircuit - Page 1 of 1



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| Body Length: |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Between 0.745 inches and 0.755 inches |
| Body Width: |
| Between 0.245 inches and 0.250 inches |
| Body Height: |
| Between 0.115 inches and 0.125 inches |
| Maximum Power Dissipation Rating: |
| 180.0 milliwatts |
| Operating Tempurature Range: |
| +0.0/+75.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Dual edge triggered and monolithic and positive outputs and resettable and w/clear |
| Inclosure Material: |
| Plastic |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Diode-transistor logic and transistor-transistor logic |
| Input Circuit Pattern: |
| Dual 5 input |
| Design Function And Quantity: |
| 2 flip-flop, j-k, clocked and 2 flip-flop, j-k, master slave |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 5.5 volts power source |
| Time Rating Per Chacteristic: |
| 50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level output and 50.00 nanoseconds. |
| output |
| Terminal Type And Quantity: |
| 16 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| Demilitarization: |
| Yes - demil/mli |

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